Product Specification

800G OSFP to 800G OSFP Active Optical Cable

PN: O800-O800-A

Features

- Hot Pluggable OSFP form factor
- Available length range 1~50m
- Active Optical Cable
- Operating data rate 850Gbps
- Single +3.3V power supply
- Max power dissipation <16W
- 8-Channel Full-Duplex Passive Copper Cable
- Commercial temperature range 0°C to 70°C

Compliance

- Compliant with OSFP MSA
- Compliant with CMIS 5.1
- RoHS

Applications

- 800G Ethernet
- Cloud Services
- Data Center Interconnect
- Data center Enterprise networking
- Switches with OSFP ports

Description

The O800-O800-A is an advanced Active Optical Cable (AOC) designed for high-density 800G connectivity in next-generation data centers and AI-driven infrastructures. Leveraging OSFP encapsulation, it supports 800Gb/s bandwidth via 8×106.25Gbps PAM4 modulation over multimode fiber, achieving transmission distances up to 50m for intra-rack and inter-rack links. Integrated with VCSEL-based transceivers and a low-power 7nm DSP chip, it ensures minimal latency (<4ns) and optimized thermal management, compatible with OSFP MSA and 800GBASE-VR8 standards. This plug-and-play solution eliminates DSP overhead, simplifies deployment, and offers backward compatibility with existing 400G ecosystems.

The O800-O800-A consuming <14W to align with energy-efficient data center requirements. Its AOC architecture provides EMI immunity, reduced signal attenuation, and cost savings compared to discrete optics, ideal for AI/ML clusters, cloud storage, and HPC environments. Compliance with IEEE 802.3bs and CMIS 4.0 ensures seamless integration into 1.6T-ready networks, while its compact design balances power tolerance (up to 15W) and thermal efficiency for sustained 800G performance.

Product performance Specifications

1、Basic Product Characteristics

Parameter	Symbol	Min	Typ.	Max	Unit
Storage Temperature	T_S	-40	-	+85	°C
Supply Voltage	V_{CC}	-0.3	3.3	3.6	V
Relative Humidity	RH	5	-	85	%
Operating Case Temperature	T_C	0	-	70	°C
Data Rate	DR	-	850	-	Gbps
Bit Error Rate	BER			$2.4x10^{-4}$	

2、Product Optical and Electrical Characteristics

Parameter	Symbol	Min	Typ.	Max	Unit
Transmitter					
Differential input impedance	Zin	90	100	110	Ω
Differential input voltage amplitude	ΔVin	400		900	mVp-p
Input Logic Level High	V_{IH}	2.0		Vcc	V
Input Logic Level Low	V_{IL}	0		0.8	V
Centre Wavelength	λc	842	850	948	nm
RMS spectral width	Δλ			0.65	nm
Average launch power, each lane	Pout	-4.6		5.5	dBm

Optical Modulation Amplitude (OMAouter), each lane	OMA			4	dBm
Transmitter and dispersion eye closure for PAM4(TDECQ),each lane	TDECQ			4.4	dB
Extinction Ratio	ER	2.5			dB
Average launch power of OFF transmitter, each lane				-30	dB
Receiver					
Differential Output impedance	Zout	90	100	110	Ω
Differential output voltage amplitude	e ΔVout			850	mVp-p
Output Logic Level High	V_{OH}	Vcc-0.5		Vcc	V
Output Logic Level Low	V_{OL}	0		0.4	V
Centre Wavelength	λc	842	850	948	nm
Receiver Sensitivity in OMAout	RXsen			max (-4.4,TECQ-6.2)	dBm
Stressed Receiver Sensitivity in OMA out	SRS			-1.8	dBm
Maximum Average power at receiver, each lane input, each lane				5.5	dBm
Minimum Average power at receiver, each lane		-6.3			dBm
Receiver Reflectance				-15	dB
LOS Assert	LOSA	-15		-8.5	dBm
LOS De-Assert	LOSD			-6.5	dBm
LOS Hysteresis	LOSH	0.5			dB

Recommended Host Board Power Supply Circuit

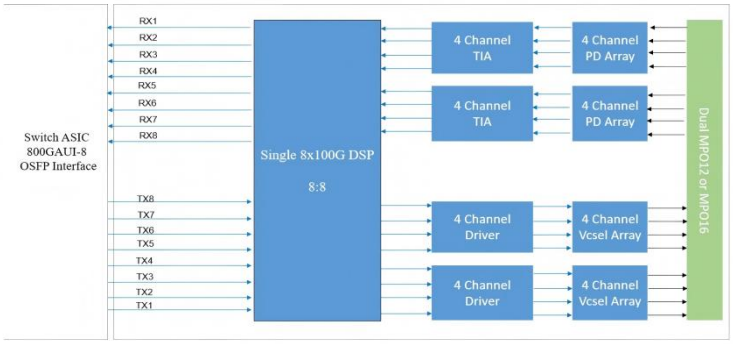


Figure 1: Module Block Diagram

Recommended Interface Circuit

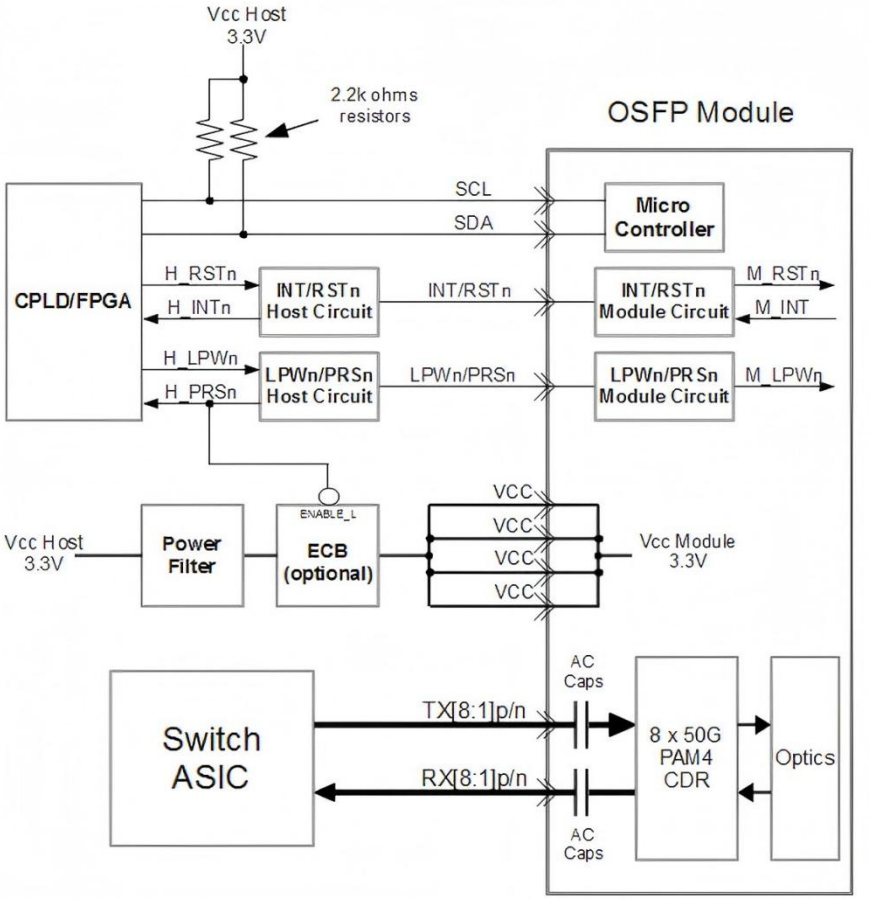


Figure2: Recommended Interface Circuit

Pin-out Definition

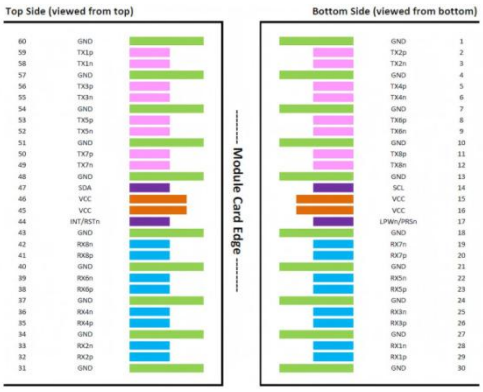


Figure3:Pin view

Pin Function Definitions

Pin	Logic	Symbol	Description	Note
1		GND	Ground	
2	CML-I	TX2p	Transmitter Data Non-Inverted	
3	CML-I	TX2n	Transmitter Data Inverted	
4		GND	Ground	
5	CML-I	TX4p	Transmitter Data Non-Inverted	
6	CML-I	TX4n	Transmitter Data Inverted	
7		GND	Ground	
8	CML-I	TX6p	Transmitter Data Non-Inverted	
9	CML-I	TX6n	Transmitter Data Inverted	
10		GND	Ground	
11	CML-I	TX8p	Transmitter Data Non-Inverted	
12	CML-I	TX8n	Transmitter Data Inverted	
13		GND	Ground	
14	LVCMOS-I/O	SCL	2-wire Serial interface clock	1
15		VCC	+3.3V Power	
16		VCC	+3.3V Power	
17	Multi-Level	LPWn/PRSn	Low-Power Mode / Module Present	2
18		GND	Ground	
19	CML-O	RX7n	Receiver Data Inverted	
20	CML-O	RX7p	Receiver Data Non-Inverted	
21		GND	Ground	
22	CML-O	RX5n	Receiver Data Inverted	
23	CML-O	RX5p	Receiver Data Non-Inverted	

24		GND	Ground	
25	CML-O	RX3n	Receiver Data Inverted	
26	CML-O	RX3p	Receiver Data Non-Inverted	
27		GND	Ground	
28	CML-O	RX1n	Receiver Data Inverted	
29	CML-O	RX1p	Receiver Data Non-Inverted	
30		GND	Ground	
31		GND	Ground	
32	CML-O	RX2p	Receiver Data Non-Inverted	
33	CML-O	RX2n	Receiver Data Inverted	
34		GND	Ground	
35	CML-O	RX4p	Receiver Data Non-Inverted	
36	CML-O	RX4n	Receiver Data Inverted	
37		GND	Ground	
38	CML-O	RX6p	Receiver Data Non-Inverted	
39	CML-O	RX6n	Receiver Data Inverted	
40		GND	Ground	
41	CML-O	RX8p	Receiver Data Non-Inverted	
42	CML-O	RX8n	Receiver Data Inverted	
43		GND	Ground	
44	Multi-Level	INT/RSTn	Module Interrupt / Module Reset	2
45		VCC	+3.3V Power	
46		VCC	+3.3V Power	
47	LVCMOS-I/O	SDA	2-wire Serial interface data	1
48		GND	Ground	
49	CML-I	TX7n	Transmitter Data Inverted	
50	CML-I	TX7p	Transmitter Data Non-Inverted	
51		GND	Ground	
52	CML-I	TX5n	Transmitter Data Inverted	
53	CML-I	TX5p	Transmitter Data Non-Inverted	
54		GND	Ground	
55	CML-I	TX3n	Transmitter Data Inverted	
56	CML-I	TX3p	Transmitter Data Non-Inverted	
57		GND	Ground	
58	CML-I	TX1n	Transmitter Data Inverted	
59	CML-I	TX1p	Transmitter Data Non-Inverted	
60		GND	Ground	

Note1: Open-Drain with pull up resistor on Host.

Note2: See pin description for required circuit.

Monitoring Specification

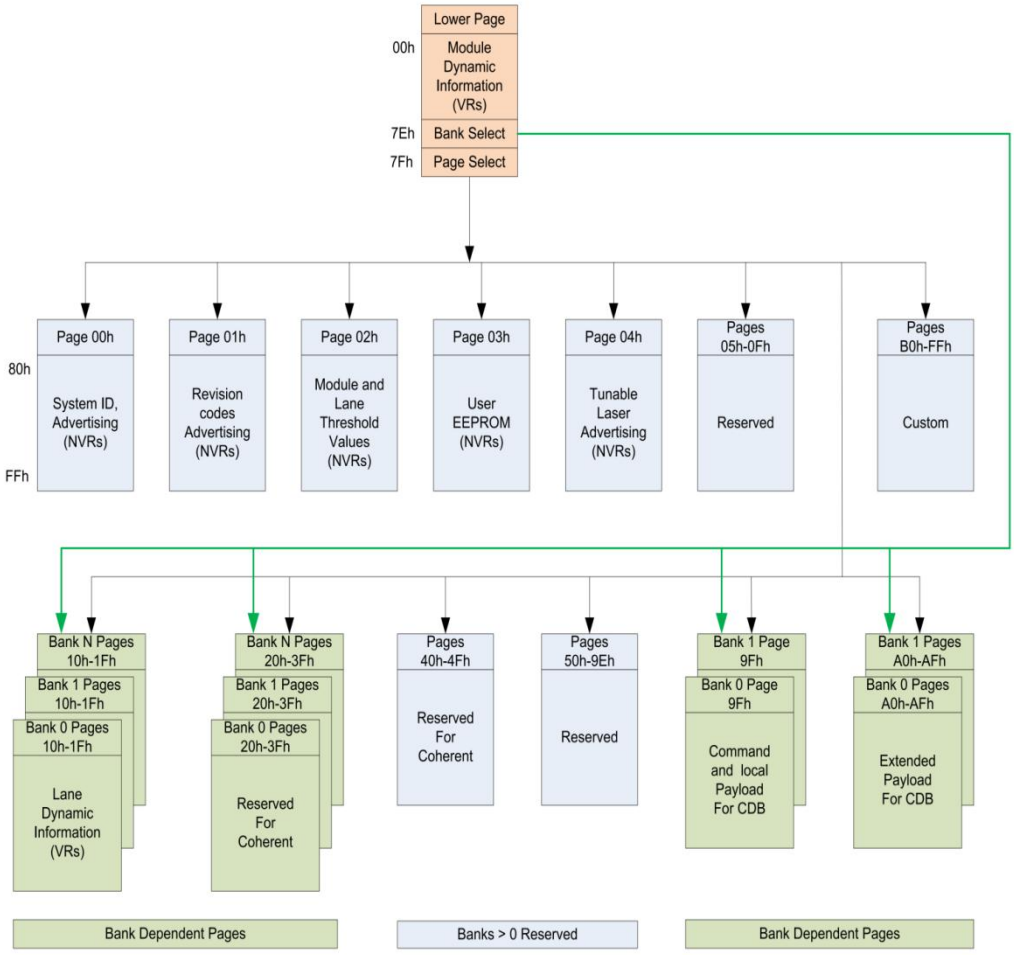


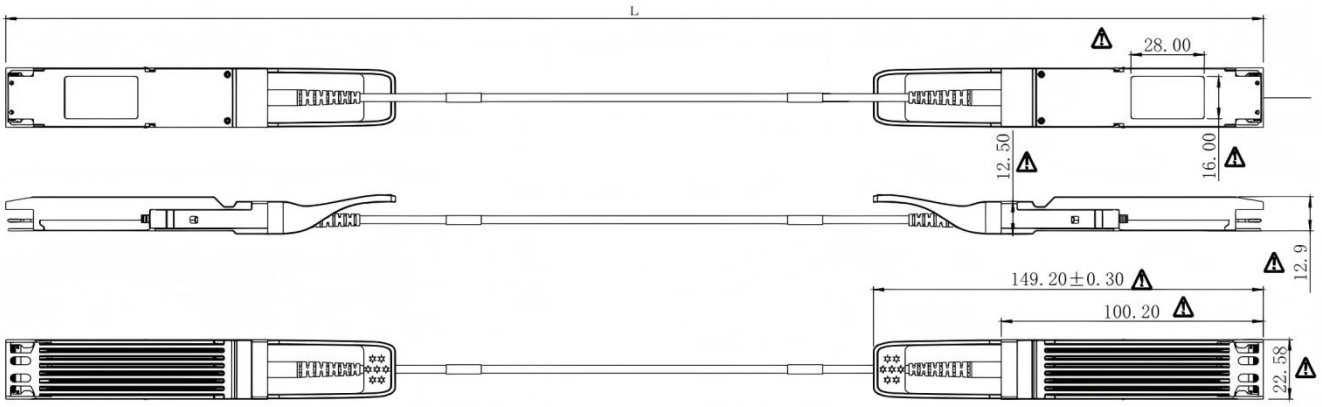
Figure4: Memory map

Memory map Table

Byte	Unit	Name	Description
Lower Page 00h			
0	1	Identifier	Identifier - Type of Serial Module - See SFF-8024.
1	1	Revision Compliance	Identifier – CMIS revision; the upper nibble is the whole number part and the lower nibble is the decimal part. Example: 01h indicates version 0.1, 21h indicates version 2.1.
2-3	2	ID and Status Area	Flat mem indication, CLEI present indicator, Maximum TWI speed, Current state of Module, Current state of the Interrupt signal.
4-7	4	Lane Flag Summary	Flag summary of all lane flags on pages 10h-1Fh.
8-13	6	Module-Level Flags	All flags that are not lane or data path specific.
14-25	12	Module-Level Monitors	Monitors that are not lane or data path specific.

26-30	5	Module Global Controls	Controls applicable to the module as a whole
31-36	6	Module-Level Flag Masks	Masking bits for the Module-Level flags
37-38	2	CDB Status Area	Status of most recent CDB command
39-40	2	Module Firmware Version	Module Firmware Version.
41-63	23	Reserved Area	Reserved for future standardization
64-82	19	Custom Area	Vendor or module type specific use
83-84	2	Inactive Firmware Version	Version Number of Inactive Firmware. Values of 00h indicates module supports only a single image.
85-117	33	Application Advertising	Combinations of host and media interfaces that are supported by module data path(s)
118-125	8	Password Entry and Change	Password Entry and Change
126	1	Bank Select Byte	Bank address of currently visible Page
127	1	Page Select Byte	Page address of currently visible Page
Upper Page 00h			
128	1	Identifier	Identifier - Type of Serial Module - See SFF-8024.
129-144	16	Vendor name	Vendor name (ASCII)
145-147	2	Vendor OUI	Vendor IEEE company ID
148-163	16	Vendor PN	Part number provided by vendor (ASCII)
164-165	8	Vendor rev	Revision level for part number provided by vendor (ASCII)
166-181	10	Vendor SN	Vendor Serial Number (ASCII)
182-183	2	Date code year	ASCII code, two low order digits of year (00=2000)
184-185	2	Date code month	ASCII code digits of month (01=Jan through 12=Dec)
186-187	2	Date code day of month	ASCII code day of month (01-31)
188-189	2	Lot code	ASCII code, custom lot code, may be blank
190-199	10	CLEI code	Common Language Equipment Identification code
200-201	2	Module power characteristics	Module power characteristics
202	1	Cable assembly length	Cable assembly length
203	1	Media Connector Type	Media Connector Type
204	1	5 GHz attenuation	Passive copper cable attenuation at 5 GHz in 1 dB increments
205	1	7 GHz attenuation	Passive copper cable attenuation at 7 GHz in 1 dB increments
206	1	12.9 GHz attenuation	Passive copper cable attenuation at 12.9 GHz in 1 dB increments
207	1	25.8 GHz attenuation	Passive copper cable attenuation at 25.8 GHz in 1 dB increments
208-209	2	Reserved	Reserved
210-211	2	Cable Assembly Lane Information	Cable Assembly Lane Information
212	1	Media Interface Technology	Media Interface Technology
213-220	8	Reserved	Reserved
221	1	Custom	Custom
222	1	Checksum	Includes bytes 128-221
223-255	33	Custom Info NV	Custom Info NV

Mechanical Dimension



Note:

- Diameter: 3mm
- Minimum bend radius:30mm
- Cable color:Orange(OM2),Aqua(OM3),Magenta(OM4)
- When L≤1m, the tolerance is +5cm
- When 1m≤L≤4.5m, the tolerance is +15cm
- When 5m≤L≤14.5m, the tolerance is +30cm
- When L≥15m, the tolerance is +2%m

Waring:

- The transceiver optics is supplied with a dust cover. This plug protects the transceiver optics during standard manufacturing processes by preventing contamination from air borne particles.It is recommended that the dust cover remain in the transceiver whenever an optical fiber connector is not inserted.
- Handling Precautions: This device is susceptible to damage as a result of electrostatic discharge (ESD). A static free environment is highly recommended. Follow guidelines according to proper ESD procedures.
- Laser Safety: Radiation emitted by laser devices can be dangerous to human eyes. Avoid eye exposure to direct or indirect radiation.